Kee IFW-



licants:

Richmond et al.

Assignee:

ZiLOG, Inc.

Title:

"Clock Controller with Clock Source Fail-Safe Logic"

Appl. No.:

10/764,391

Filing Date: January 23, 2004

Examiner:

James F. Sugent

Art Unit: 2116

Docket No.: ZIL-555

March 27, 2007

Mail Stop RCE COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450

REQUEST FOR CONTINUED EXAMINATION (RCE)

Dear Sir:

This is a Request for Continued Examination (RCE) under 37 C.F.R. §1.114 of the above-identified application. Please consider the Amendment that is being filed herewith. A check is enclosed that includes the amount of \$790.00 for the RCE fee required under 37 C.F.R. §1.17(e).

Please contact the undersigned attorney with any questions concerning this request or the above-identified patent application.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450,

Alexandria, VA 22313-1450.

Date of Deposit: March 27, 2007

Respectfully submitted,

Darien K. Wallace Attorney for Applicant

Reg. No. 5397/36/2007 TBESHAH1 00000040 10764391

Customer No. 47.漏13

790.00 OP